



**SB-0623**

**First Year B. Sc. Examination**  
**March/April – 2011**  
**Electronics Paper : I**  
**(Computer Science)(Basic Electronics)**  
*(Old Course)*

Time : 3 Hours]

[Total Marks : 70

**Instructions :**

(1)

नीचे दर्शावेक निशानीवाणी विगतो उत्तरवही पर अवश्य लपवी.  
Fillup strictly the details of signs on your answer book.

Name of the Examination :  
F. Y. B. Sc.

Name of the Subject :  
Electronics Paper : 1

Subject Code No. : 0 6 2 3 Section No. (1, 2.....): NIL

Seat No. :  
[ ] [ ] [ ] [ ] [ ] [ ]

Student's Signature

- (2) All questions are compulsory.
- (3) Assume date if necessary.
- (4) Symbol and abbreviations have their usual meaning
- (5) Figures to the right indicate full marks.

1 Answer in brief : 14

- (i) Define tolerance of resistor.
- (ii) What is Q-factor of capacitor ?
- (iii) Define "Operating point".
- (iv) Convert 64 decimal in to binary.
- (v) What is OR law of Boolean algebra ?
- (vi) Write the truth table of NAND gate.
- (vii) Compare ROM and RAM.

2 (a) Describe various types of junction capacitance of diode. 7  
(b) Explain construction and types of inductor. 7

**OR**

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**1**

**[Contd...**

<b>2</b>	(a) Explain operation of BJT.	<b>6</b>
	(b) How transistor work as switch.	<b>5</b>
	(c) Give the color code of following resistor :	<b>3</b>
	(i) 1K ohms	
	(ii) 10 K ohms	
	(iii) 1 M ohms	
<b>3</b>	(a) Explain binary addition, subtraction, multiplication and division with example.	<b>8</b>
	(b) Explain working of photo diode in detail.	<b>6</b>
	<b>OR</b>	
<b>3</b>	(a) Prove De' Mogan's theorem.	<b>8</b>
	(b) Prove the following Boolean identity.	<b>6</b>
	$(A+B)(A+C)=A+BC$	
<b>4</b>	(a) Design full adder and subtractor and explain it.	<b>8</b>
	(b) Draw the TTL circuit and explain circuit operation.	<b>6</b>
	<b>OR</b>	
<b>4</b>	(a) How Flip Flop working as memory element? Detail explain JK FF .	<b>8</b>
	(b) Briefly explain EPROM.	<b>6</b>
<b>5</b>	Write notes : (any two)	<b>14</b>
	(i) Types of capacitor	
	(ii) MOSFET	
	(iii) Logic Gate	
	(iv) Ripple Counter.	